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FORM PTO-1390 (REV. 1-2000)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTORNEY'S DOCKET NUMBER 46484-00003
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. APPLICATION NO. 10/030310 (35 U.S.C. 371(c)(1)(5))	
INTERNATIONAL APPLICATION NO. PCT/KR00/00718	INTERNATIONAL FILING DATE 05 July 2000	PRIORITY DATE CLAIMED 10 July 1999 17 May 2000	
TITLE OF INVENTION AUXILIARY STORE SELECTING CIRCUIT FOR USE IN COMPUTER SYSTEM WITH MULTIFUNCTION AND SELECTING METHOD THEREFOR			
APPLICANT(S) FOR DO/EO/US Seung Wook PARK			
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:			
<p>1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371.</p> <p>2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371.</p> <p>3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).</p> <p>4. <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.</p> <p>5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2))</p> <p>a. <input checked="" type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau).</p> <p>b. <input type="checkbox"/> has been transmitted by the International Bureau.</p> <p>c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US)</p> <p>6. <input type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)).</p> <p>7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))</p> <p>a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau).</p> <p>b. <input type="checkbox"/> have been transmitted by the International Bureau.</p> <p>c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.</p> <p>d. <input type="checkbox"/> have not been made and will not be made.</p> <p>8. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).</p> <p>9. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)) (unsigned)</p> <p>10. <input type="checkbox"/> An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5))</p> <p>Items 11. to 16. below concern other document(s) or information included:</p> <p>11. <input type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.</p> <p>12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.</p> <p>13. <input type="checkbox"/> A FIRST preliminary amendment.</p> <p>14. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment.</p> <p>15. <input type="checkbox"/> A substitute specification.</p> <p>16. <input type="checkbox"/> A change of power of attorney and/or address letter.</p> <p>17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13.2 and 35 U.S.C. 1.821 - 1.825.</p> <p>18. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4).</p> <p>19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).</p> <p>20. <input checked="" type="checkbox"/> Other items or information:</p> <p>COPY OF THE INTERNATIONAL SEARCH REPORT PREPARED BY THE KOREAN INDUSTRIAL PROPERTY OFFICE; COPY OF THE INTERNATIONAL PRELIMINARY EXAMINATION REPORT; AND CONFIRMATION POSTCARD.</p>			

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U.S. APPLICATION NO. 10/030310 <small>(if known, see 37 CFR 1.51(a))</small>	INTERNATIONAL APPLICATION NO. PCT/KR00/00718	ATTORNEY'S DOCKET NUMBER 46484-00003
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17. <input checked="" type="checkbox"/> The following fees are submitted: Basic National Fee (37 CFR 1.492(a)(1)-(5)): Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO \$1,040.00 International preliminary examination fee NOT paid to USPTO but International Search Report prepared by the EPO or JPO \$890.00 International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search fee (37 CFR 1.445(a)(2)) paid to USPTO \$710.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) \$690.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) \$100.00 <div style="text-align: right;">ENTER APPROPRIATE BASIC FEE AMOUNT =</div>	CALCULATIONS	PTO USE ONLY																																																																														
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<table border="1" style="width:100%; border-collapse: collapse;"> <tr> <th style="width:25%;">Claims</th> <th style="width:15%;">Number Filed</th> <th style="width:15%;">Number Extra</th> <th style="width:15%;">Rate</th> <th style="width:15%;"></th> <th style="width:15%;"></th> </tr> <tr> <td>Total Claims</td> <td>12 - 20 =</td> <td>0</td> <td>x \$18.00</td> <td>\$</td> <td>0</td> </tr> <tr> <td>Independent Claims</td> <td>4 - 3 =</td> <td>1</td> <td>x \$84.00</td> <td>\$</td> <td>84</td> </tr> <tr> <td colspan="3">Multiple dependent claims(s) (if applicable)</td> <td>+ \$280.00</td> <td>\$</td> <td>0</td> </tr> <tr> <td colspan="4" style="text-align: right;">TOTAL OF ABOVE CALCULATIONS =</td> <td>\$</td> <td>1124</td> </tr> <tr> <td colspan="4">Reduction by 1/2 for filing by small entity, if applicable. Verified Small Entity statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28).</td> <td>\$</td> <td></td> </tr> <tr> <td colspan="4" style="text-align: right;">SUBTOTAL =</td> <td>\$</td> <td>1124</td> </tr> <tr> <td colspan="4">Processing fee of \$130.00 for furnishing the English translation later the <u> 20 </u> <u> 30 </u> months from the earliest claimed priority date (37 CFR 1.492(f)).</td> <td>\$</td> <td></td> </tr> <tr> <td colspan="4" style="text-align: right;">TOTAL NATIONAL FEE =</td> <td>\$</td> <td>1124</td> </tr> <tr> <td colspan="4">Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +</td> <td>\$</td> <td></td> </tr> <tr> <td colspan="4" style="text-align: right;">TOTAL FEES ENCLOSED =</td> <td>\$</td> <td>1124</td> </tr> <tr> <td colspan="4"></td> <td style="text-align: right;">Amount to be: refunded</td> <td>\$</td> </tr> <tr> <td colspan="4"></td> <td style="text-align: right;">charged</td> <td>\$</td> </tr> </table>	Claims	Number Filed	Number Extra	Rate			Total Claims	12 - 20 =	0	x \$18.00	\$	0	Independent Claims	4 - 3 =	1	x \$84.00	\$	84	Multiple dependent claims(s) (if applicable)			+ \$280.00	\$	0	TOTAL OF ABOVE CALCULATIONS =				\$	1124	Reduction by 1/2 for filing by small entity, if applicable. Verified Small Entity statement must also be filed. (Note 37 CFR 1.9, 1.27, 1.28).				\$		SUBTOTAL =				\$	1124	Processing fee of \$130.00 for furnishing the English translation later the <u> 20 </u> <u> 30 </u> months from the earliest claimed priority date (37 CFR 1.492(f)).				\$		TOTAL NATIONAL FEE =				\$	1124	Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				\$		TOTAL FEES ENCLOSED =				\$	1124					Amount to be: refunded	\$					charged	\$		
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a. ☒ A check in the amount of \$ 1124 to cover the above fees is enclosed.
 b. ☐ Please charge my Deposit Account No. 10-0447 in the amount of \$ to cover the above fees. A duplicate copy of this sheet is enclosed.
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NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

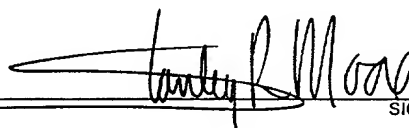
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WO 01/04731

PCT/KR00/00718

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AUXILIARY STORE SELECTING CIRCUIT FOR USE IN COMPUTER SYSTEM WITH MULTIFUNCTION AND
SELECTING METHOD THEREFORBACKGROUND OF THE INVENTION

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Field of the Invention

The present invention relates to a computer system, and more particularly, to an auxiliary store selecting circuit in a computer system with multifunction, and its selecting method.

10

Discussion of Related Art

In general, it is well known and utilized a microcomputer including a personal computer and its workstation, as an information processing system which receives constant information and processes it on the basis of a given procedure and outputs its result. In the computer system provided as a common designation of a hardware in such computer, a software necessary for its operation and an application system concerned with its utilization; a systematic environment such as an operating system and a use language etc. is much various, and its users are also classified into various sorts according to its usage.

15

Multiplex user system and multitasking system are herein disclosed to meet various requirements of computer system users and are well known in a conventional technique. This multiplex user system is the computer system that several users can use its equipments at the same time, and this multitasking system is the computer system that several works can be executed simultaneously. For example, however, in case one computer system is purchased for one home, there maybe is an environmental difference due to its usage that parent uses one computer and a child uses it. In order to execute a specific work though

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they use the same environment, it is needed to install a system environment different from a current using environment. In this case, there is no any settlement in the conventional system. In other words, the computer system of the present time can not well provide multifunction that the above requirements can be satisfied in case that a plural number of users use one computer system in mutually different environments at different time.

A hard disk drive as an auxiliary memory can be installed additionally and a CMOS set-up can be executed, in the conventional computer system, but such installment and execution are almost to extend a capacity of data on the basis of an expert's viewpoint and are the concept different from multifunction in which numerous users can smoothly use the computer system in different environments at different time so as to construct the multifunction.

Therefore it is required a technique through which a plural number of users can smoothly utilize one computer system in mutually different systematic environments at different time. For example, further, parent may work at their desired systematic environment in case data stored in the hard disk drive etc. of the computer system is damaged owing to a mistake use of a child, if the requirements are settled.

Some technique is also required to definitely protect the stored information or data from a hacking or computer virus, in a connective environment between a computer system and a communication network such as an internet, the connective environment is provided according that the communication network is widely spread.

SUMMARY OF THE INVENTION

It is, therefore, a primary object of the present invention to

provide a circuit technique through which several users can smoothly use one computer system in mutually different environments at different time.

Another object of the present invention is to provide an auxiliary store selecting circuit and its selecting method, in which
5 each user can use an independent auxiliary memory to protect the auxiliary memory and information of other users therefrom.

A still another object of the present invention is to provide an auxiliary store selecting circuit and its selecting method, in which
10 a continuous selection for any one hard disc out of numerous hard discs is valid during a power-on just through a manipulation of an external switch in one computer system which has an installment of numerous hard discs for the sake of the construction of multifunction.

A further another object of the invention is to provide an auxiliary store selecting circuit capable of definitely protecting the
15 stored information or data from a hacking or computer virus, in a connective environment between a computer system and a communication network such as an internet.

To achieve the above objects and advantages, in accordance with
20 the present invention, an auxiliary store selecting circuit adequate to a computer system to construct an inventive multifunction comprises a selective switch part for generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation; a selection maintaining part for maintaining the
25 generated selection signal till a power-off even though there is any selective manipulation after the generation of the selection signal in the selective switch part, and then outputting it; and a selective connection part for performing a connection between the selected

auxiliary memory and a computer main board in response to the output selection signal from the selection maintaining part.

The auxiliary store selecting method in a computer system on which a plurality of auxiliary memories are mounted to construct the multifunction, includes the steps of: generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation; holding the generated selection signal till a power-off; and performing an electric connection between a computer main board and an auxiliary memory to be selected, in response to the held selection signal.

In such inventive construction one out of a plural number of hard disks in one computer system can be selected to execute the work required by a user, thereby obtaining the construction of multifunction, namely, enabling each user to individually use the auxiliary memory so as to protect auxiliary memories and information of other users therefrom, and further protecting the computer system from the hacking or computer virus even in connecting the computer system to a communication network such as an internet.

BRIEF DESCRIPTION OF THE ATTACHED DRAWINGS

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

In the drawings:

Fig. 1 represents a visual diagram of a computer system with

multifunction in accordance with the present invention;

Fig. 2 indicates a block diagram of an auxiliary store selecting circuit in the preferred embodiment of the present invention;

Fig. 3 depicts a detailed circuit diagram showing one embodiment
5 of an auxiliary store selecting circuit in the invention;

Fig. 4 sets forth a connected-pin disposition diagram of a connection part shown in Fig. 2;

Fig. 5 illustrates a detailed circuit diagram showing another
embodiment of the auxiliary store selecting circuit shown in Fig. 2;
10 and

Fig. 6 provides a disposition diagram for connected-pins and circuit elements in another embodiment of the connection part shown in Fig. 2.

15 DETAILED DESCRIPTION OF PREFERRED EMBODIMENT

The present invention is described in detail as follows, referring to the accompanying drawings which represent an auxiliary store selecting circuit proper to a computer system with multifunction and indicate its selecting method. In the drawings the same or similar
20 functional blocks are indicated by the same or similar reference numbers.

Fig. 1 depicts a visual diagram of a computer system with multifunction in accordance with the present invention. In Fig. 1, the computer system includes a computer main body(10) on which a
25 microprocessor and a main memory are mounted, a plurality of hard disk drives(50,51,52) as the auxiliary memory, floppy disk drives(2,4), an auxiliary store selecting circuit(100) for selecting one out of the hard disk drives(50,51,52), a key board(30) as an input unit, a mouse(40)

and a monitor(20) as a display output unit. A printer etc. as a printing output unit can be also connected thereto through the computer main body(10) and a cable.

In Fig. 1 for the inventive preferred embodiment, the auxiliary store selecting circuit(100) is installed on a substantially upper part of the computer main body(10), and manipulation parts of selective switches(S1,S2,S3) corresponding to respective hard disk drives in a case of three hard disk drives(50,51,52), and front faces of light emitting diodes(LED)(D4,D5,D6) corresponding to the respective selective switches(S1,S2,S3), are seen as the external view thereof, the selective switches being for performing its selection and the light emitting diodes(LED) being for selectively performing luminescence.

Fig. 2 shows a schematic block diagram of an auxiliary store selecting circuit(100) shown in Fig. 1. Referring to Fig. 2, the auxiliary store selecting circuit(100) is composed of a selective switch part(110) for generating a selection signal for selecting one out of a plurality of auxiliary memories in response to a selective manipulation; a selection maintaining part(120) for maintaining the generated selection signal till a power-off even though there is any selective manipulation after the generation of the selection signal in the selective switch part(110), and then outputting it; and a selective connection part(130) for performing a connection between the selected auxiliary memory and a computer main board in response to the output selection signal from the selection maintaining part(120). In the drawing, cables(C1,C2,C3) having numerous connection lines(for example, forty connection lines in a case of forty pins) connect hard disk drives(50,51,52) as the auxiliary memory to a connector part(135). A cable(C4) takes charge of a connection between the connector part(135)

and a computer main board(11), and a cable(C5) is provided as four lines in a case of three hard disk drive(50,51,52) and takes charge of a connection between the selective switch part(110) and the connector part(135).

5 Fig. 3 sets forth a detailed circuit diagram of one embodiment in the auxiliary store selecting circuit(100) shown in Fig. 2. In case there are three hard disk drives, the selective switch part(110) is composed of three selective switches(S1,S2,S3) having a two-circuit three-key interlocking method. For example, in case a user tries to
10 select a first hard disk drive(50), he selects and presses the selective switch(S1) and then switch nodes N3 and N6 are connected and N2 and N5 are connected, each other. Then switch nodes N1 and N4 are opened each other. A selection signal is generated by such manipulation in the selective switch part(110). Lines HIN28, H1 28, H2 28, H3 28 shown in
15 the selective switch part(110) correspond to the cable(C5) of Fig. 2 and also mean jumper lines as a cable select. A number 28 in the hard disk drive based on a general IDE(Integrated Development Environment) system represents a pin to transmit a signal for recognizing one designated hard disk drive from a microprocessor of a computer.

20 The selection maintaining part(120) is made up of a flipflop integrated circuit(IC1), resistances (R7,R8,R9,R10,R11,R12,R13), light emitting diodes(D4,D5,D6) and a capacitor(C4) so as to maintain the generated selection signal till a power-off and output it, in case the selective switch part(110) is constructed with three selective
25 switches(S1,S2,S3). Herewith a reference number B+ represents supply voltage of about 5V generally. The light emitting diodes(D4,D5,D6) are indicated as the same reference numbers in the visual diagram of Fig. 1, which is to visually display for the user that the hard disk drive

of what times was currently selected. A latched output selection signal provided from one out of lines(S11,S22,S33) respectively connected between one side ends of the resistances(R10,R11,R13) and respective anodes of the LEDs(D4,D5,D6) is provided as a "high" level. In other words, in case the selective switch(S1) is selected, a high signal is continuously provided during a power-on on the line(S11) among the lines(S11,S22,S33), and a "low" signal is provided on each of the lines(S22,S33). In this case the LED(D4) out of the LEDs(D4,D5,D6) luminesces. The selection signal generated by a latch function of the flipflop integrated circuit(IC1) is maintained till a power-off, even though there is another switch's selective manipulation in a continuous power-on state after the selection signal is first generated by the manipulation of the switch(S1).

In this embodiment, a custom integrated circuit "74LS175" is used as the flipflop integrated circuit(IC1) and the resistances (R7,R8,R9,R10,R11, R12,R13) have each value of 4.7k Ω , 1k Ω , 1k Ω , 147 Ω , 147 Ω , 1k Ω and 147 Ω . The LEDs(D4,D5,D6) are used as general LEDs, and the capacitor(C4) has a value of 22 μ F. These elements may be replaced by other equivalent elements if there is other case, and the resistance value and a capacity value can be also changed herein.

The selective connection part(130) for performing a connection between the selected auxiliary memory and the computer main board(11) is composed of resistances(R1,R2,R3,R4,R5,R6), capacitors(C1,C2,C3), transistors(Q1,Q2,Q3), circuit protecting diodes(D1,D2,D3), and relays(RL1,RL2,RL3). In case the high signal is applied onto only the line(S11), only the transistor(Q1) among the transistors(Q1,Q2,Q3) becomes a turn-on state to thus drive only the relay(RL1). Thereby the pin 23 of the hard disk drive HDD1, namely, H1 23, is connected to the

pin 23 of the main board(11), namely, HIN 23, each other, and the pin 25 of the hard disk drive HDD1, H1 25, is connected to the pin 25 of the main board(11), HIN 25, wherein the pin 23 is to provide read data and the pin 25 is to provide write data. Consequently, the pins 23 and 25 of one hard disk drive selected among three hard disk drives are electrically connected with the respective pins 23 and 25 of the computer main board(11). Also the pins 23 and 25 of the hard disk drives not selected are isolated from the respective pins 23 and 25 of the computer main board(11), whereby the desired objects can be obtained.

In this embodiment, the resistances (R1,R2,R3,R4,R5,R6) have each value of 2k Ω , 10k Ω , 2k Ω , 10k Ω , 2k Ω and 10k Ω , and the capacitors(C1,C2,C3) have each a value 22 μ F. All the transistors(Q1,Q2,Q3) are constructed by each bipolar transistor "2SC1815". All the diodes(D1,D2,D3) are provided as each "1N4148" and all the relays(RL1,RL2,RL3) are provided as each custom 5V 4pin relay. Such elements may be replaced by other equivalent elements in case there exists other case, and the resistance value and the capacity value can be also changed herein.

Fig. 4 illustrates a connected-pin disposition diagram of the connector part(135) shown in Fig. 2 and shows a disposition state of pins in the connector part connected with the hard disk drives in case there are three hard disk drives. Each of pins 23,25,28 in a set 4 is electrically isolated one another in an individual connector, and corresponds to and are connected to a selected pin 3 of HIN through an operation of the relays mounted on the printed circuit board. This pins' disposition is compatible with and applicable to the hard disk drive of the future E(Enhanced)IDE system almost without change.

Another inventive embodiment is described referring to Figs. 5

and 6, as follows.

In another preferred embodiment concerning of Fig. 2, with reference to Fig. 5 it shows a detailed circuit of the auxiliary store selecting circuit(100). The detailed constructive elements of Fig. 5 have a little different points from them shown in Fig. 3, thus the corresponding circuit blocks are represented as similar reference numbers.

Referring to Fig. 5, in case there are three hard disk drives, a selective switch part(110a) is composed of three selective switches(S1A,S1B,S1C) having a two-circuit three-key interlocking method. Herewith the selective switches(S1A,S1B,S1C) correspond respectively to three selective switches(S1,S2,S3) of Fig. 1, with a performance of the same function. For example, in case a user tries to select a first hard disk drive(50), he selects and presses the selective switch(S1A) and then the switch nodes N3 and N6 are connected and N2 and N5 are connected, each other. Then the switch nodes N1 and N4 are opened each other. A selection signal is generated by such manipulation in the selective switch part(110a). In case the selective switch part(110a) is composed of three selective switches(S1A,S1B,S1C), a selection maintaining part(120a) is made up of a flipflop integrated circuit(U1), resistances(R1,R2,R3,R7), light emitting diodes(LED1,LED2,LED3) and a capacitor(C1) so as to maintain the generated selective signal till a power-off and output it. Herewith, operating supply voltage is about 12V. The light emitting diodes(LED1,LED2,LED3) respectively correspond to the LEDs(D4,D5,D6) shown in the visual diagram of Fig. 1, which visually informs the user of which hard disk drive was selected. A latched output selection signal provided from one out of the lines(S11,S22,S33) respectively

connected between one side ends of the resistances(R1,R2,R3) and respective anodes of the LEDs(LED1,LED2,LED3) is provided as a "high" level. In other words, in case the selective switch(S1A) is selected, a high signal is continuously provided during a power-on on the line(S11) among the lines(S11,S22,S33), and a "low" signal is provided on each of the lines(S22,S33). In this case the light emitting diode(LED1) out of the LEDs(LED1,LED2,LED3) luminesces. The selection signal generated by the latch function of the flipflop integrated circuit(U1) is maintained till a power-off, even though there is other switch's selective manipulation in a continuous power-on state after the selection signal is first generated by the manipulation of the switch(S1).

In this embodiment, it is used the flipflop integrated circuit(U1) as a custom integrated circuit "4042", and the resistances(R1,R2,R3,R7) have each value of $1k\Omega$, $1k\Omega$, $1k\Omega$ and $4.7k\Omega$. The LEDs(LED1,LED2,LED3) are used as general LEDs, and the capacitor(C1) has a value of $22\mu F$. Such elements may be replaced by other equivalent elements in case there is other cases, and the resistance value and the capacity value can be also changed herein. A reference number "HPIN" of the selection maintaining part(120a), which is not described here, represents a connecter and takes charge of a connection between lines. The selection maintaining part(120a) operates at about 12V, differently from an operation of the selection maintaining part(120) of Fig. 3 driven by the operating voltage of 5V, therefore an on-resistance in switching is reduced to represent a reduced noise level, which ensures an operational reliability by a definite switching operation.

The selective connection part(130a) for performing a connection between the selected auxiliary memory and the computer main board(11)

is composed of resistances(R8,R9), transistors(Q1,Q2), circuit protecting diodes(D1,D2) and relays(RL1,RL2). In case the high signal is applied onto only the line(S11), only the transistor(Q1) among the transistors(Q1,Q2) becomes a turn-on state to thus drive only the relay(RL1). Whereby the pin 23 of the hard disk drive HDD1, namely, H1 23, is connected to the pin 23 of the main board(11), namely, HIN 23, each other, and the pin 25 of the hard disk drive HDD1, H1 25, is connected to the pin 25 of the main board(11), HIN 25, each other.

Herewith the pin 23 is to provide a read control signal and the pin 25 is to provide a write control signal. Consequently, the pins 23 and 25 of one hard disk drive selected among three hard disk drives are electrically connected with each of the pins 23 and 25 of the computer main board(11), and the pins 23 and 25 of the hard disk drives not selected are isolated from each of the pins 23 and 25 of the computer main board(11), whereby the inventive desired object can be obtained. The resistances(R8,R9) have each value of 6.8k Ω and 6.8k Ω , and all the transistors(Q1,Q2) are constructed as each NPN type bipolar transistor "2SC1815". All the diodes(D1,D2) are provided as each "1N4148" and all the relays(RL1,RL2) are provided as each custom 12V 8pin relay. Such elements may be replaced by other equivalent elements in case there exists other cases, and the resistance value and the capacity value can be also changed herein. The selective connection part(130a) has the small number of elements in its components in comparison with the construction of the selective connection part(130) of Fig. 3, that is, can obtain the compact construction, together with characteristics of a prominent noise immunity and operating voltage of 12V for the sake of an operational reliability.

Fig. 6 provides a disposition diagram for connected-pins and

circuit elements in another embodiment of the connector part shown in Fig. 2. In Fig. 6, connectors(135-1,135-2,135-3,135-4)are installed to perform the same function as the element 135 of Fig. 4. Reference numbers CN1,CN2,CN3 represent connectors for use of displayer
5 connection, and R1,R2,R3 are connected with the connectors for use of the connection, the connectors being coupled with the LED and all these resistors R1,R2,R3 having a resistance value of $1k\Omega$. Reference numbers 137 and 138 indicate connectors for use of a power connection. In Fig. 6 a switch block 136 is additionally installed in comparison with the
10 case of Fig. 4. Such additional installment of the switch block(136) settles an inconvenience of operating the jumper wire as the afore-mentioned cable select in achieving the inventive object. That is to say, it is released from an inconvenience of operating the jumper wire to decide a master/slave in the hard disk drive. This switch
15 block(136) is constructed with four switches(SW1-SW4) and each one switch((SW1) among them can be embodied by an integrated circuit element "4066" IC for an IDE control.

In Fig. 6, also, the master can be selectively used through a control of a terminal for use of the IDE(HDD/CD) without needing a
20 manipulation of a user. For example, as the replacement of the main board in which a function of a chip select(C3) is not smoothly supplied in an ATA(AT bus attachment) as a new standard of the IDE, the master/slave can be selectively used to settle the defects. A required hard disk drive can be selectively used by efficiently connecting and
25 cutting off a pin concerned with a detection among IDE pins. In this case, an IRQR(Interrupt Request) of a number 31, a PDIAG of a number 34, a CS0 of a number 37, a CS1 of 38 and a DASP of 39 are additionally controlled. The connection and cutting-off operations is gotten by

maintaining the line of the selected side as an on-state and the line not selected as an off-state through a use of the selection signal in the flipflop.

That is, in case it is decided to need to construct the multifunction by a user in his intension that the computer will be used by other persons including the user himself and several environments need to be built up therein, in purchasing one computer system, the user can purchase and install a plural number of hard disk drives and mount the above-mentioned auxiliary store selecting circuit(100) as shown in Fig. 1. Then, in using it, when only an external switch is simply manipulated and one out of the drives is continuously selected unless a main power switch(6) shown in Fig. 1 is turned off, namely, during a power-on.

According to that, it is available to build up a desired environmental construction by the number of hard disk drives installed, in other words, for example, an OS environment and an environment according to a usage can be distinguished by the respective drives in its construction, wherein the OS environment is such as Win95-98, Win NT, OS/2, Linux, Unix, Win2000, etc. and the environment according to the usage is as game, work and graphic usages, etc. Also the construction can be distinguished according to a plurality of respective users. In such function, each independent work can be done by making one computer to get several kinds of environmental constructions and an advantage like a case that the user uses several computers can be gotten by just installing the hard disk drive. Also in case the computer system is connected to the communication network such as an internet and one specific hard disk among a plurality of hard disks is connected as a communication network usage, data stored in the rest hard disks

is protected from a hacking or computer virus. Thus, the other system can be protected therefrom in case numerous computer systems are connected through the LAN.

Accordingly, in accordance with the present invention, the auxiliary store selecting circuit and its selecting method can be widely applied to a case of a trader or a student studying abroad who uses several languages, and also to cases that numerous users should utilize one computer at home or office, a computer should be utilized for uses of a work, a game, a graphic working and a communication, namely, should be used in a classified working state, and also an emergence hard disk drive is required at places, for example, such as a game room etc. where computers should continuously operate for 24 hours.

In accordance with the present invention, as afore-mentioned, desired works can be separately executed by selecting one out of a plurality of hard disks in one computer system, to thereby obtain the construction of multifunction. In addition, every user can use an independent auxiliary memory to whereby protect the auxiliary memory and information of other users.

Further, in case that the computer system is connected to a communication network such as an internet and one specific hard disk among numerous hard disks is connected thereto as a communication network usage, data stored in the rest other hard disks can be protected from a hacking or computer virus. That is, there is relatively an advantage in a protection of information even though a plural number of computer systems are connected by a local area network(LAN) one another.

Although the invention has been shown and described with respect to the preferred embodiments, it will be understood by those skilled

in the art that various changes and modifications may be made without departing from the spirit and scope of the invention as defined in the following claims. For example, only the hard disk drive as the auxiliary memory was herein described as one example but replacement
5 elements such as an EEPROM, a magnetic drum, etc. may be used as the auxiliary memory.

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What is claimed is:

1. An auxiliary store selecting circuit adequate to a computer system with multifunction, comprising:

5 a selective switch part for generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation;

10 a selection maintaining part for maintaining the generated selection signal till a power-off even though there is the selective manipulation after the generation of the selection signal in the selective switch part, and then outputting it; and

15 a selective connection part for performing a connection between the selected auxiliary memory and a computer main board in response to the selection signal outputted from the selection maintaining part.

2. The circuit of claim 1, wherein said selective switch part is composed of selective switches based on a 2-circuit 3-key interlocking method by the number corresponding to the number of the auxiliary memories.

20 3. The circuit of claim 1, wherein said selection maintaining part includes at least a flipflop integrated circuit connected to said selective switch part.

25 4. The circuit of claim 1, wherein said selective connection part comprises a relay for performing an electric connection between specific pins of the auxiliary memory selected and specific pins of a computer main board, and a transistors for use of a driving, for driving said

relay.

5. The circuit of claim 4, wherein said specific pins includes a read pin and a write pin.

5

6. A method for selecting an auxiliary memory in a computer system on which a plural number of auxiliary memories are mounted to construct multifunction, comprising the steps of:

generating a selection signal for selecting one out of numerous auxiliary memories in response to a selective manipulation;

10

holding the generated selection signal till a power-off; and

performing an electric connection between the auxiliary memory to be selected and a computer main board in response to the selection signal held.

15

7. The method of claim 6, wherein said auxiliary memory is a hard disk drive based on an IDE system.

8. A hard disk drive selecting circuit adequate to a computer system having an installment of a plurality of hard disk drives, comprising:

20

a selection switching unit for generating a selection signal for selecting one out of the hard disk drives in response to a selective manipulation of a user;

25

a selection maintaining unit having a latch circuit thereinside, for continuously maintaining the selection signal of the selection switching unit during a power-on, and then outputting it; and

a selection connecting unit for performing an electric

connection between read and write pins of the hard disk drive selected and its corresponding pins of a computer main board in response to the selection signal outputted from the selection maintaining unit.

5 9. The circuit of claim 8, wherein said selection maintaining unit further comprises a light emitting diode to visually display and inform the outside of that a specific hard disk drive was selected in response to a selective manipulation.

10 10. An auxiliary store selecting circuit adequate to a computer system with multifunction, comprising:

a selective switch part for generating a selection signal for selecting one out of numerous auxiliary memories installed in the computer system in response to a selection;

15 a selection maintaining part for maintaining the generated selection signal till a power-off even though there is the selective manipulation after the generation of the selection signal in the selective switch part, and then outputting it; and

20 a selective connection part including a connector unit for automatically performing a connection between the selected auxiliary memory and a computer main board in response to the selection signal outputted from the selection maintaining part.

25 11. The circuit of claim 10, wherein said selection maintaining part includes at least a flipflop integrated circuit connected to said selective switch part and operating at about 12V.

12. The circuit of claim 10, wherein said connector unit contains

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FIG.1

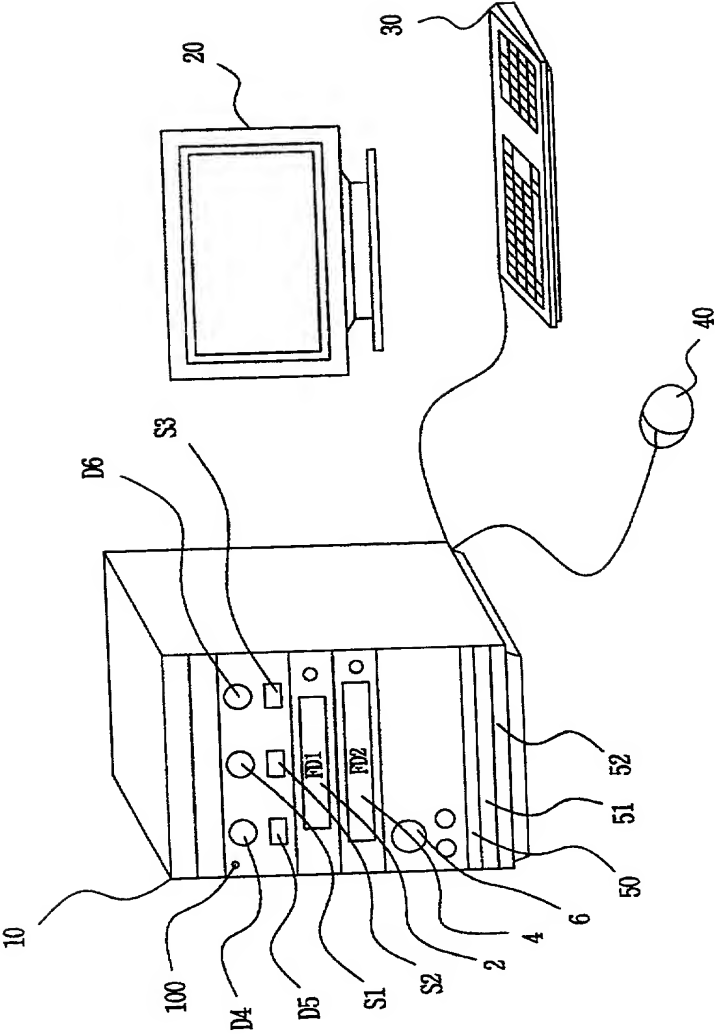
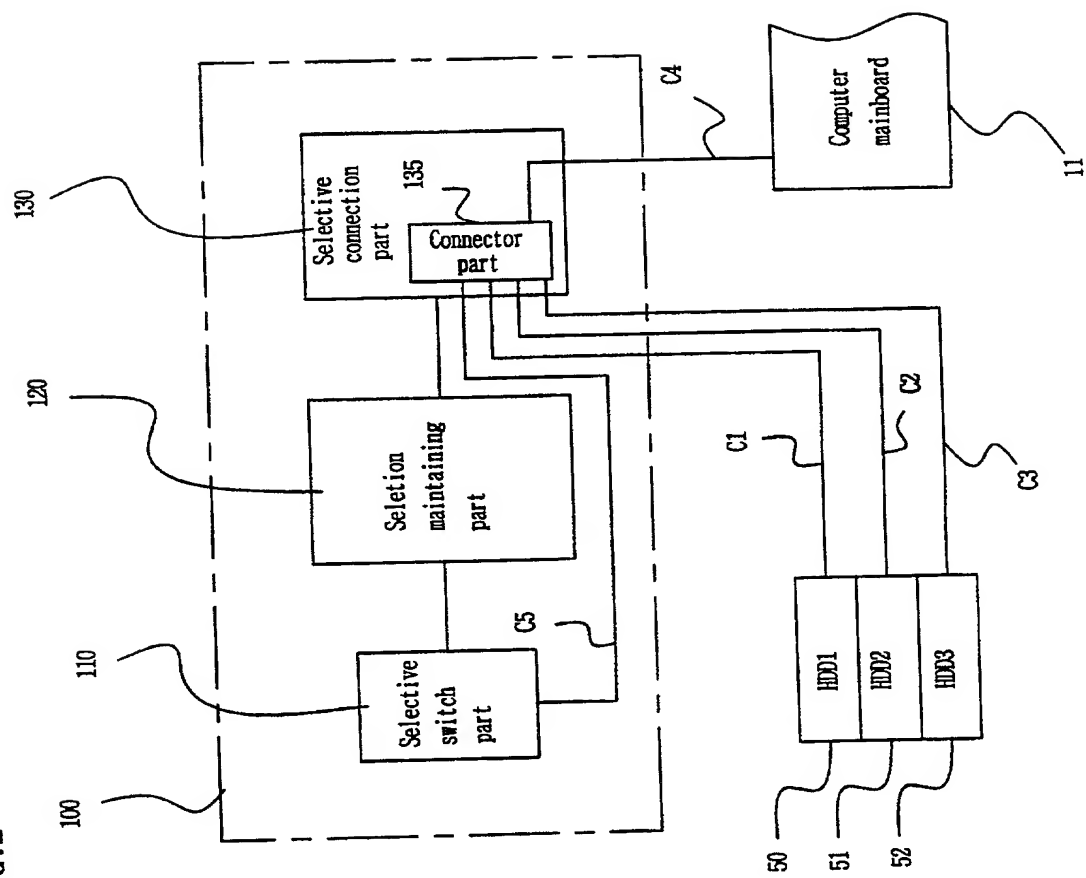


FIG. 2



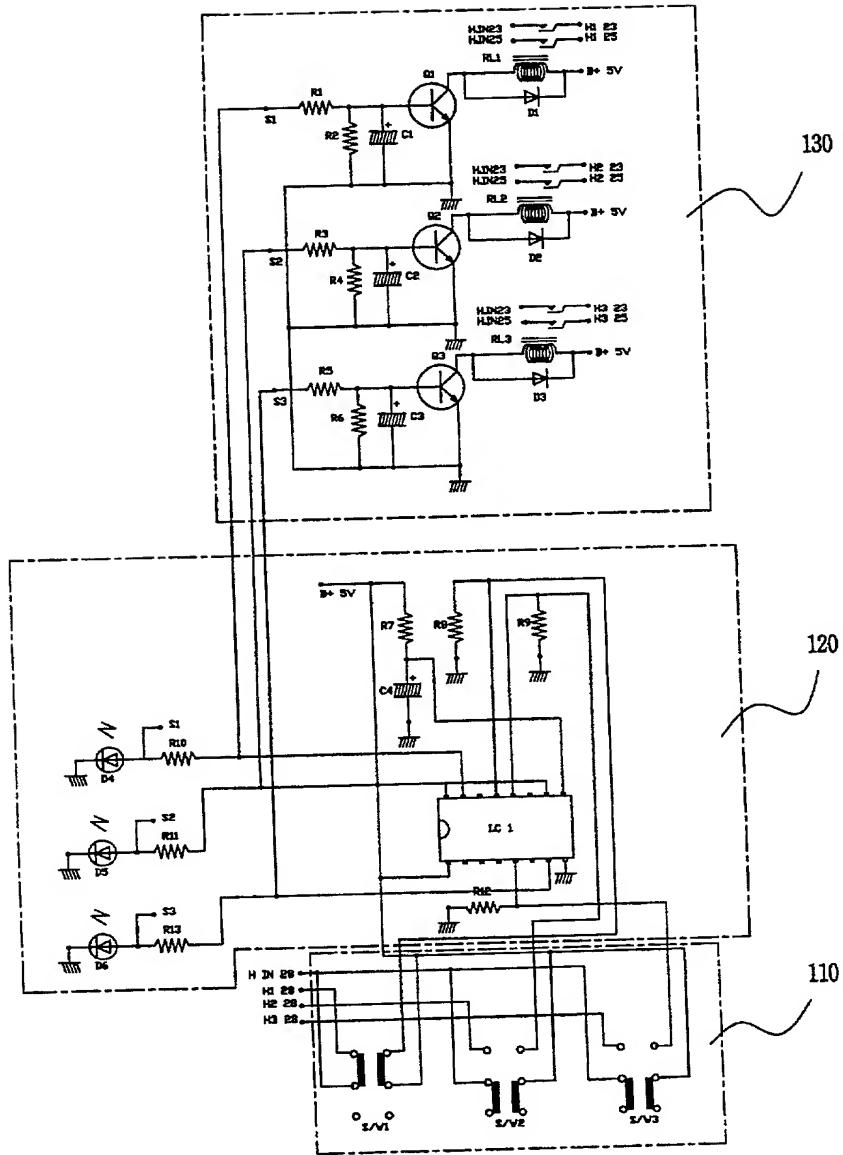


FIG. 3

FIG. 4

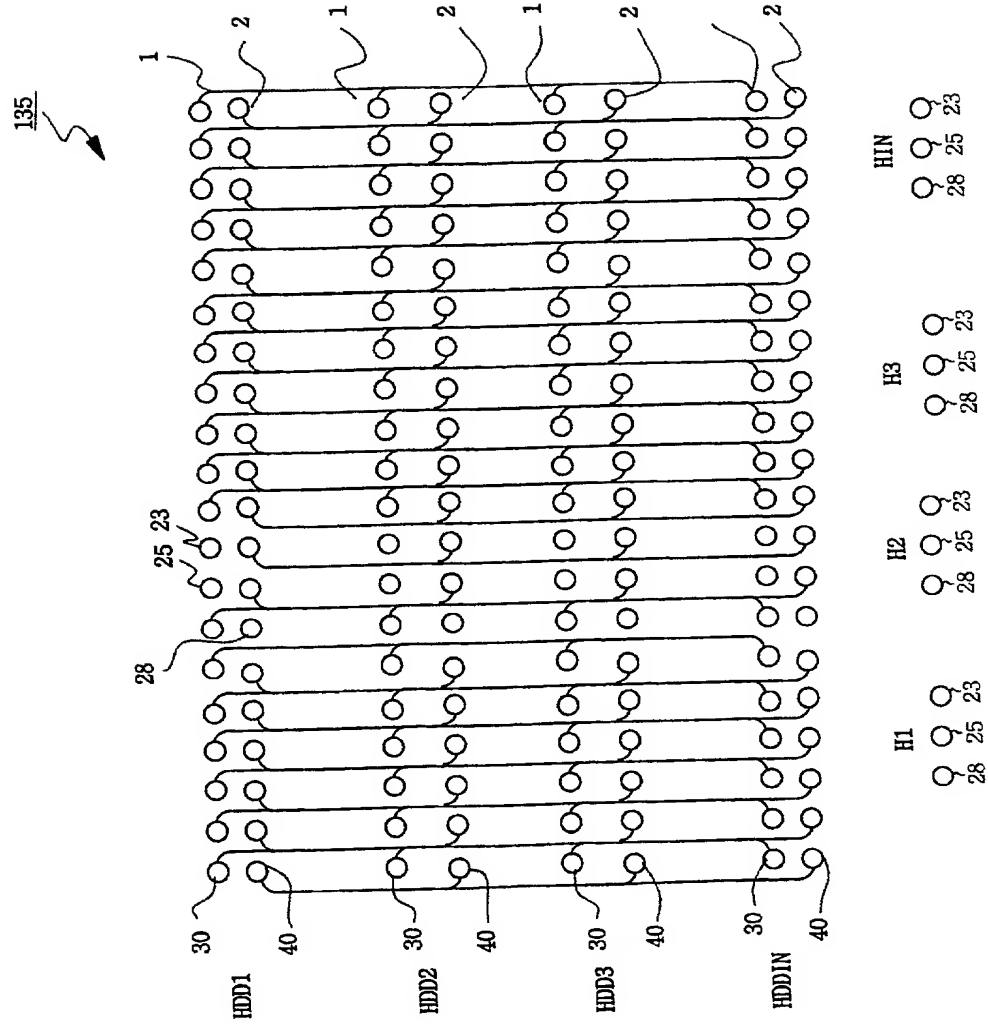


FIG. 5

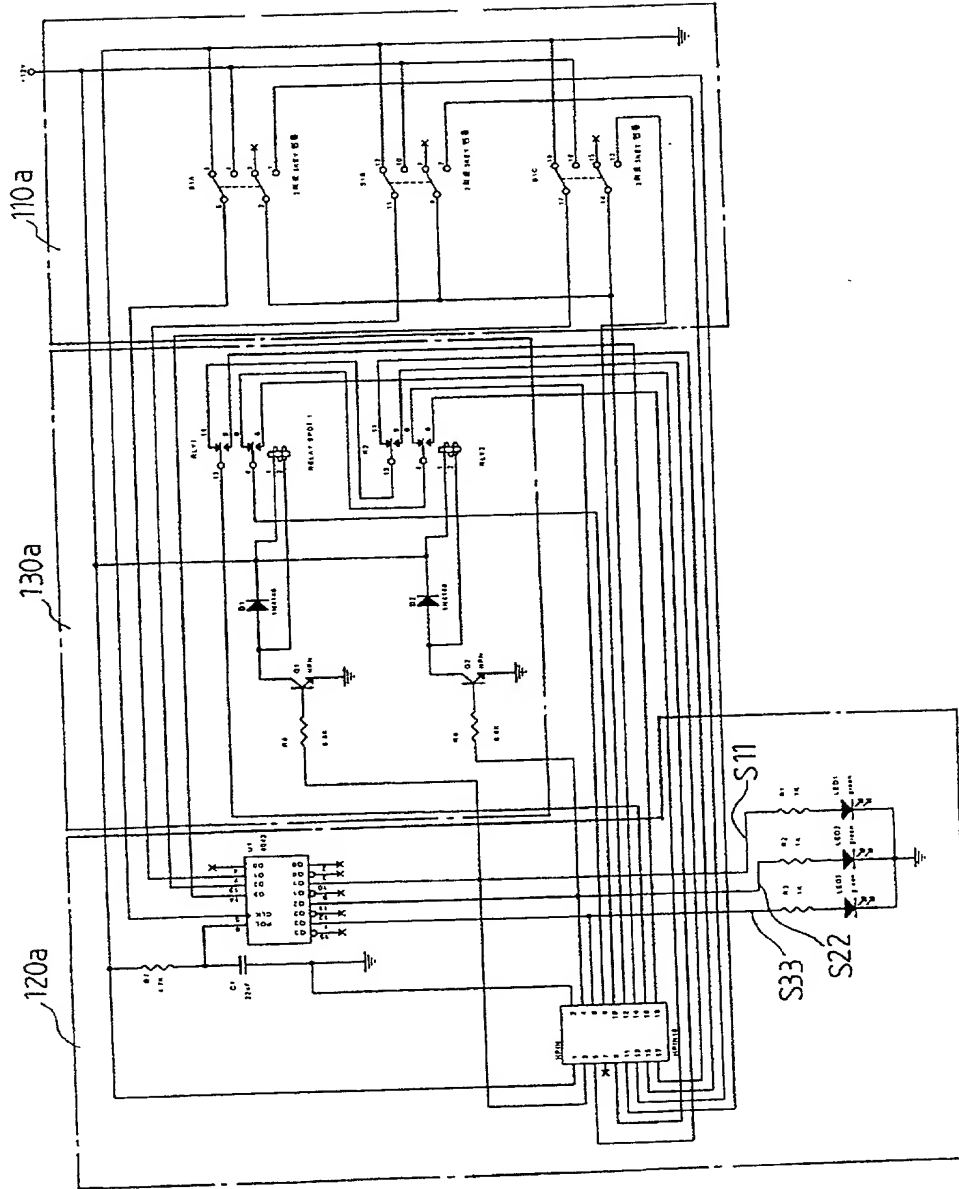
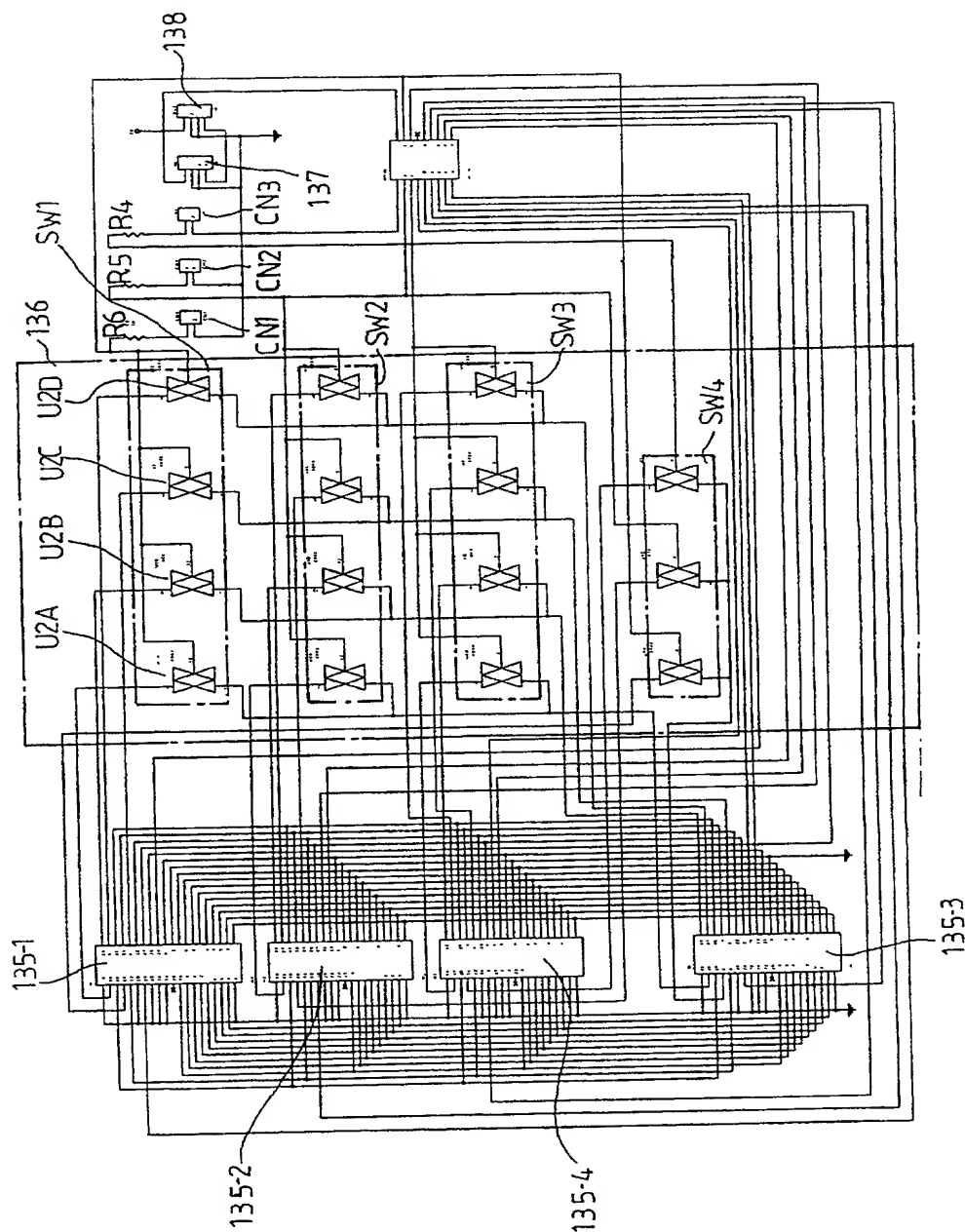


FIG. 6



PATENT APPLICATION
ATTORNEY DOCKET NO.: 46484-00003

RULES 63 AND 67 (37 C.F.R. 1.63 and 1.67)
DECLARATION AND POWER OF ATTORNEY

FOR UTILITY/DESIGN/CIP/PCT NATIONAL APPLICATIONS

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;
and

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: AUXILIARY STORE SELECTING CIRCUIT FOR USE IN COMPUTER SYSTEM WITH MULTIFUNCTION AND SELECTING METHOD THEREFOR, the specification of which: (mark only one)

- ___ (a) is attached hereto.
___ (b) was filed on ___ as Application Serial No. ___ and was amended on ___ (if applicable)
___ (c) was filed as PCT International Application No.: PCT/___ on ___, ___ and was amended on ___ (if applicable).
___ (d) was filed on ___ as Application Serial No. ___ and was issued a Notice of Allowance on ____.
X (e) was filed on January 3, 2002, and bearing attorney docket number 46484-00003.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above or as allowed as indicated above.

I acknowledge the duty to disclose all information known to me to be material to the patentability of this application as defined in 37 CFR § 1.56. If this is a continuation-in-part (CIP) application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

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I hereby claim foreign priority benefits under 35 U.S.C. § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which my priority is claimed or, (2) if no priority is claimed, before the filing date of this application:

PRIOR FOREIGN PATENTS

Number	Country	Month/Day/Year Filed	Date first laid-open or Published	Date patented or Granted	Priority Claimed	
					Yes	No
1999/27924	Korea	July 10, 1999			XX	
2000/26361	Korea	May 17, 2000			XX	

I hereby claim the benefit under 35 U.S.C. § 120/365 of any United States application(s) listed below and PCT international applications listed above or below:

PRIOR U.S. OR PCT APPLICATIONS

Application No. (series code/serial no.)	Month/Day/Year Filed	Status(pending, abandoned, patented)
PCT/KR00/00718	July 5, 2000	pending

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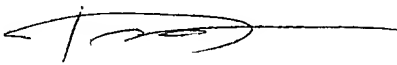
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NAMED INVENTOR(S)

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